

15/072103
02/08/2002

BEST AVAILABLE COPY

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10072103	02/08/2002	350	38	3504	J. CHO

**APPLICANTS: Verdoorn David; Woodward Sandra;

**CONTINUING DATA VERIFIED:

NONE for

** FOREIGN APPLICATIONS VERIFIED:

NONE for

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ROC920010224US1
Verified and Acknowledged Examiners's initials		
TITLE : Method and apparatus for debugging a chip		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	First Claim for
		1	0.0
ISSUE FEE		DRAWINGS	
Amount Due	Date Paid	Sheets Drawn	Pages Drawn
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368; Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

FILED WITH: ☒ DISK (CRF) ☐ CD-ROM
(Attached in pocket on right inside flap)